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FACSIMILE COVER SHEET

To Commissioner for Patents	Total Pages Sent: 3
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Facsimile Number: (703) 746-4000	Transmission Date: February 24, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Proell, *et al.* Docket No.: INF-120
Serial No: 10/733,332 Art Unit: 2818
Date Filed: December 12, 2003
Title: Integrated DRAM Semiconductor Memory and Method for Operating the Same

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- Request for Confirmation of Information Disclosure Statement (1 page)
- Copy of PTO/SB/08a (1 page)

Respectfully submitted,


Kristin R. Hayes
Legal Assistant

Confirmation Respectfully Requested

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Filed: December 12, 2003

Examiner: Lam, David

For: Integrated DRAM Semiconductor Memory and Method for Operating the
Same

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Request For Confirmation of Information Disclosure Statement

Dear Sir:

The patent application for the above-referenced patent application was allowed on January 13, 2005. An Information Disclosure Statement (copy attached) was filed by Applicant on January 6, 2005. However, Applicant did not receive confirmation of consideration of the Information Disclosure Statement by the Examiner with the Notice of Allowance. Applicant kindly requests that the Office forward the initialed copy of the PTO/SB/08A.

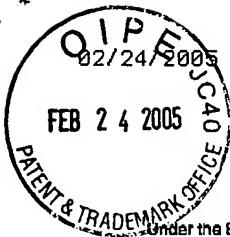
Respectfully submitted,

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Attorney for Applicant
Reg. No. 35,272

February 24, 2005

Date

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Complete if Known

Application Number	10/733,332
Filing Date	12/12/2003
First Named Inventor	Proell, et al.
Art Unit	2818
Examiner Name	TBD
Attorney Docket Number	INF-120

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	
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